

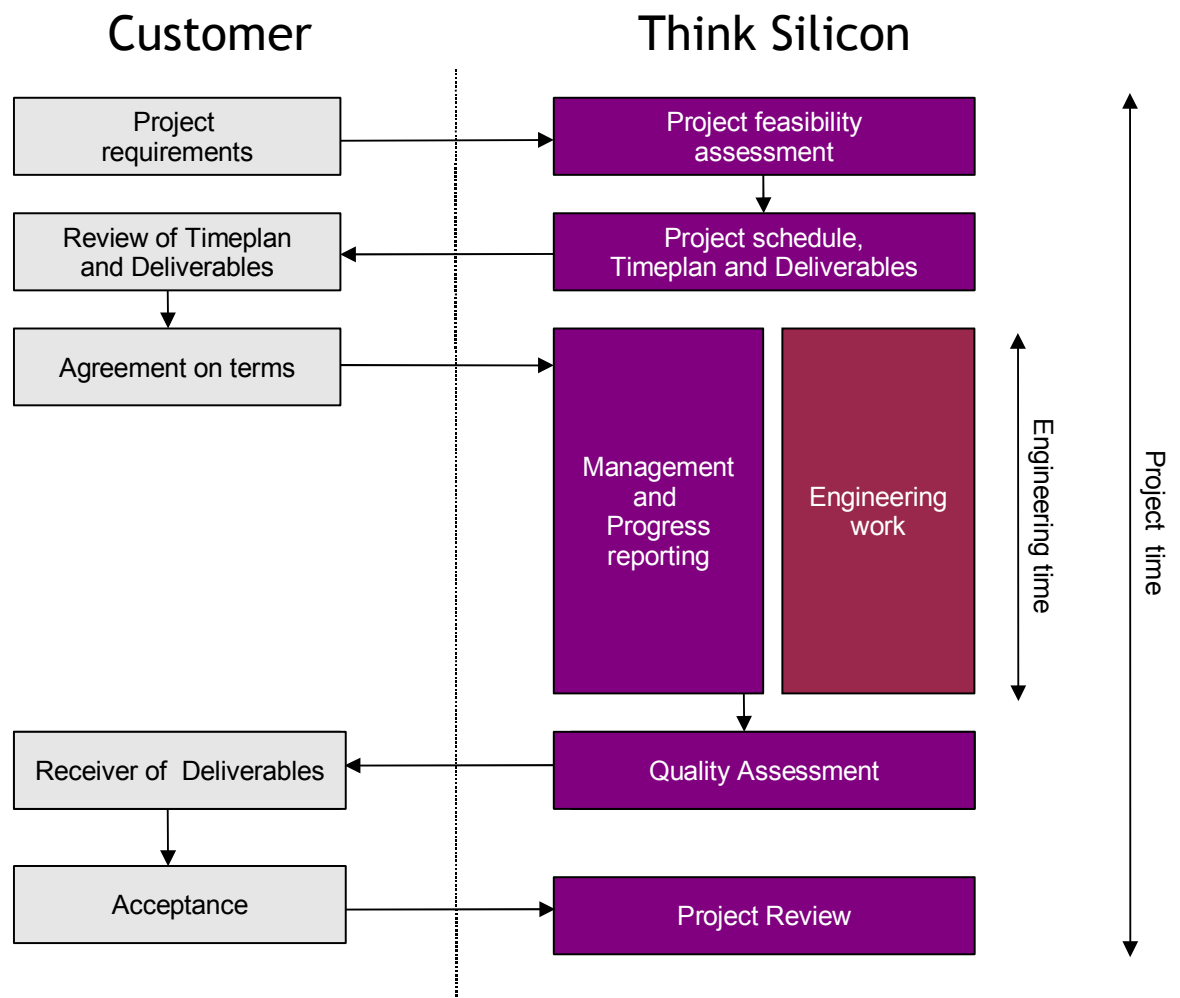
Design Services

Our Team

Think Silicon was founded by a team of highly qualified and experienced engineers from Semiconductor and established telecommunications companies. We are committed to provide solutions of the highest quality to the chip design industry. Regardless of your requirements or the complexity of the task we can assure to provide professional services of the highest level. Our engineers have a solid grasp of industry requirements and a holistic approach to the design flow of integrated circuits.

Quality at Every Level

We are focused on ensuring quality at every level of the design process. Our company procedures ensure that quality is catered for at every level of the design process, with problems identified early in the design process and quality solutions are provided. We have a special System Quality verification team that inspects design flow, results and deliverables at every stage of design process to ensure customers receive very high quality of results.



Solutions

Think Silicon provide design solutions on the following fields to the Semiconductor Industry:

Design-for-Testability

We can ensure that your device is testable to a predefined rate of coverage. We can insert scan chains in a pre-synthesised netlist, design test controlling circuits, generate test patterns and vectors.

- Scan Insertion
- ATPG
- BSDL
- JTAG

System-on-Chip

SoC (System-on-Chip) ready solutions. Think Silicon can design a complete SoC device including processors, buses, peripheral I/O so you can focus on your core business and don't worry about the rest of the System.

- AMBA Bus (APB, ASB, AHB)
- ARM processors (ARM7TDMI, ARM926, ARM946)
- Leon CPU Processor

Low-power design

We can ensure your device consumes the minimum amount of power:

- Clock Gating
- Voltage Islands
- Multi VT synthesis
- Power Estimation

Protocols

We can assist you with issues on the following protocols:

- WiFi 802.11abg
- Bluetooth
- PCI
- PCI Express
- UART, I2C, I2S, TWI
- USB
- PC Card
- AES, DES/3DES, WEP, TKIP MIC, SHA-1 and MD5
- Ethernet 10/100
- SDIO

Rates

Our robust design methodology and efficient techniques allow us to provide highly competitive rates to the industry. Our standard rates are inclusive of project management, quality control and peer inspection of deliverables. Licensing costs vary on tooling requirements.

Promise

If a project is late for whatever reason than the agreed timetables, we will cover all additional work and licensing costs.

After Sales Support

If you have any questions or need support at a later date, we will be happy to assist you.

Design Services

We can design complex circuitry using hardware description languages to the required specifications

- VHDL
- Verilog
- System Verilog

Static Timing Analysis (Sign-off)

In today's low geometry devices static timing analysis with closer look to submicron issues is of the highest importance towards a successful sign-off

- Static Timing Analysis
- OCV checks
- SI checks

Verification

We can write elaborated testbenches that exercise your module. Where applicable our in-house Bfm modules or from third parties can be used verify the functionality of your design

- VIP Models
- Bfm modules
- Formal Verification

Custom Scripting

Complex source file, netlist and result file manipulation often requires the use of custom scripting to process. We can write custom script using the following languages

- Tcl/Tk
- Perl
- Unix Shell
- Makefiles

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