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## **Think Silicon's heterogeneous NEMA GPU-WEAR project has been granted € 1.34 M (\$1.56) from EU's Horizon 2020 SME Instrument program**

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NEMA GPU-WEAR is a novel platform to develop and implement ultra-low power, heterogeneous, multicore Graphics Processing Unit (GPU) technology, for SoCs (System on the Chip). The platform is designed, to extend battery life by decreasing the power-consumption by **an order of magnitude** for the next generation of mobile, wearable and embedded devices while running fast, graphically rich display and 4K video applications.

The scrutinizing SME Instrument program is directed towards highly innovative SMEs (Small Medium Enterprise) and awarded a grant over € 1.34 M (\$1.56) to advance the technology readiness level and commercialization of the NEMA-ultra-low power graphics platform.

Think Silicon, already known as the innovative market-leader of ultra-low power GPUs for small display devices, takes the next step with NEMA GPU-WEAR, bringing the world's first real ultra-low power optimized graphics processing platform to medium-size display products (5.5"- 9.0") with lower ASPs and continued appeal in emerging and fast growing markets. The NEMA-GPU WEAR platform consists of GPGPU IP, Smart Analysis & Visualization tools, NEMAIgfx API, Graphics Software Library, GUI Development Tool-Kit, Run Time System, Compiler and **intend to support Vulkan™ API, OpenGLIES™ API and Android™ Wear OS**. The SW-tool platform will be available for the Open Developer Community for best practice.

“Current GPUs used in wearable and embedded devices, are scaled down architectures, once designed for a class of products with immersive multimedia capabilities but also requiring big batteries and still depleting them too rapidly. NEMA GPU WEAR provides a holistic approach to optimize and improve graphics and system performance and battery usage at all levels, between a new bottom-up designed GPU

architecture, CPU, display technology and software applications.” said co-founder and CTO Iakovos Stamoulis.

The first version of the NEMA GPU-WEAR evaluation kit including FPGA test board and SDK will be available in Q1-2017.

## **Acknowledgement**

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## **About Think Silicon:**

Think Silicon S.A. (TSi) is a privately held Limited Company founded in 2007, located in Patras, Greece (HQ), Toronto, Canada (Business Development & Marketing office) and San Jose, CA, USA (Sales office). The Think Silicon team specializes in developing high performance graphics IP technology for ultra-low power and area limited IoT applications.

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